

# SPP15P10P G-VB Datasheet

# P-Channel 100 V (D-S) MOSFET

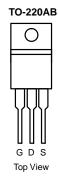
PRODUCT	SUMMARY		
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (Ω)</b>	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
- 100	0.100 at V <sub>GS</sub> = - 10 V	- 23	11.7
- 100	0.120 at $V_{GS}$ = - 4.5 V	- 20	11.7

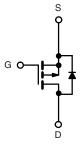
## FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- Trench Power MOSFET
- 100 %  $R_q$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

## **APPLICATIONS**

- Power Switch
- DC/DC Converters





P-Channel MOSFET

ABSOLUTE MAXIMUM RATING	<b>S</b> T <sub>C</sub> = 25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 100	v
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
Continuous Drain Current ( $T_1 = 150 \ ^{\circ}C$ )	T <sub>C</sub> = 25 °C	1-	- 23	
Continuous Drain Current $(T_j = 150^{\circ} C_j)$	T <sub>C</sub> = 70 °C	I <sub>D</sub>	- 16	A
Pulsed Drain Current		I <sub>DM</sub>	- 70	A
Avalanche Current		I <sub>AS</sub>	- 18	
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	16.2	mJ
	T <sub>C</sub> = 25 °C	P	52.1 <sup>b</sup>	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	– P <sub>D</sub> –	2.5	- W
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	50	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	3.9	0/10

Notes:

a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).



COMPLIANT HALOGEN

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 V, I_{D} = -250 \mu A$	- 100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 2.5	v
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA
		$V_{DS} = -100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			- 50	
		$V_{DS}$ = - 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 150 °C			- 250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -10 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	- 15			А
	Р	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.6 A		0.100		0
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.4 A		0.120		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 3.6 A		12		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			1055		pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = - 50 V, f = 1 MHz		65		
Reverse Transfer Capacitance	C <sub>rss</sub>			41		
Total Gate Charge <sup>c</sup>	Qg	$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -3.6 \text{ A}$		23.2	34.8	
Iotal Gale Charge				11.7	17.6	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = - 50 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 3.6 A		3.5		no
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			4.8		
Gate Resistance	Rg	f = 1 MHz	1.2	5.7	11.5	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			7	14	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = - 50 V, $R_L$ = 17.2 $\Omega$		12	18	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_{D}\cong$ - 2.9 A, $V_{GEN}$ = - 10 V, $R_{g}$ = 1 $\Omega$		33	50	115
Fall Time <sup>c</sup>	t <sub>f</sub>			9	18	
Drain-Source Body Diode Ratings and	nd Characteri	istics T <sub>C</sub> = 25 °C <sup>b</sup>				
Continuous Current	۱ <sub>S</sub>				- 8.8	۸
Pulsed Current	I <sub>SM</sub>				- 15	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 2.9 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.5	V
Reverse Recovery Time	t <sub>rr</sub>			50	75	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	А
Reverse Recovery Charge	Q <sub>rr</sub>	1 1		98	147	nC

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

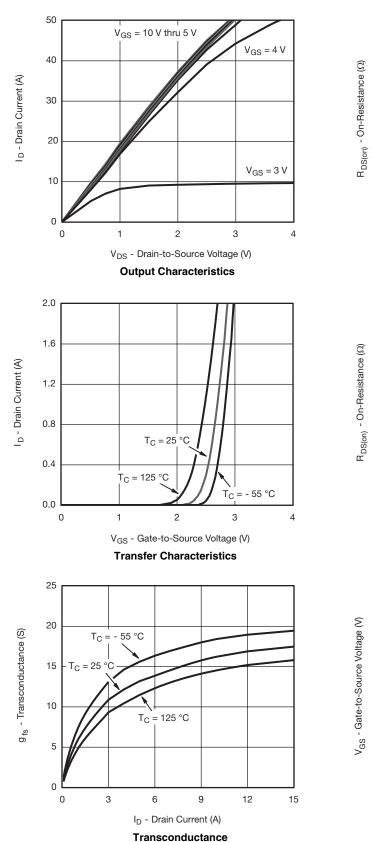
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



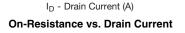
 $V_{GS} = 10 V$ 

12

15



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



6

9

0.25

0.20

0.15

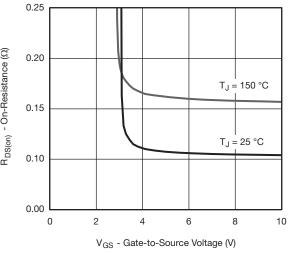
0.10

0.05

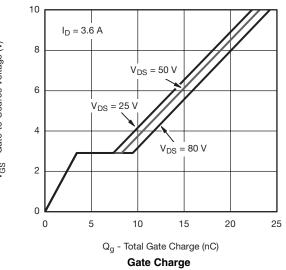
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V<sub>GS</sub> = 4.5 V

3



On-Resistance vs. Gate-to-Source Voltage



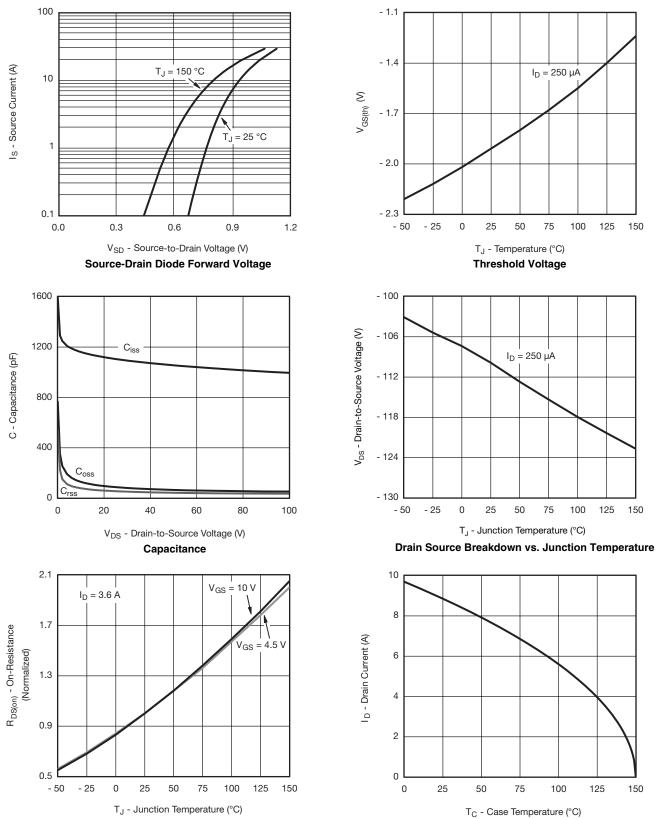


150

150

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

**On-Resistance vs. Junction Temperature** 



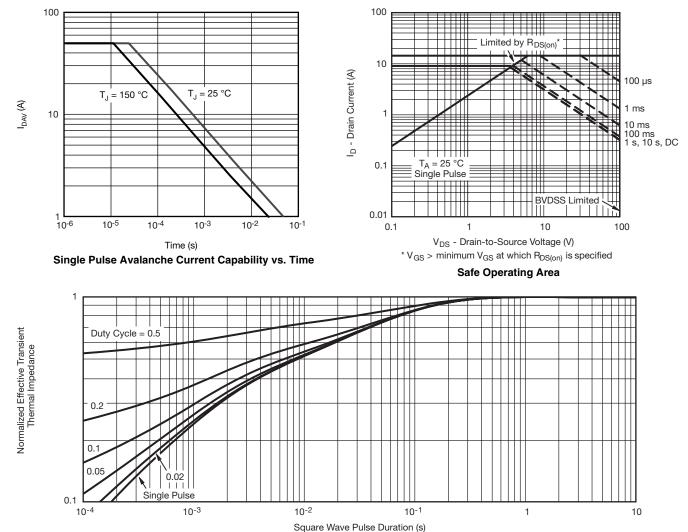
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**Current Derating** 

150



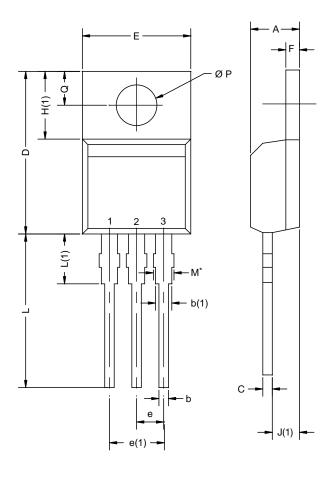
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Case



# **TO-220AB**



	MILLIN	MILLIMETERS		HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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